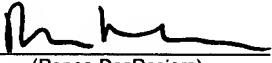


O I P E 149381  
OCT 23 2007  
PATENT & TRADEMARKS

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the U.S. Postal Service as Express Mail, Label No. EM 053196370 US, on the date shown below in an envelope addressed to: MS RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Dated: October 23, 2007      Signature:   
(Renee DesRosiers)

Docket No.: 60803 (49381)  
(PATENT)

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Kazuya Fujita et al.

Application No.: 10/773,809

Confirmation No.: 8452

Filed: February 5, 2004

Art Unit: 2814

For: SOLID STATE IMAGING DEVICE,  
SEMICONDUCTOR WAFER, OPTICAL  
DEVICE MODULE, METHOD OF SOLID  
STATE IMAGING DEVICE FABRICATION,  
AND METHOD OF OPTICAL DEVICE  
MODULE FABRICATION

Examiner: W. S. Louie

### RESPONSE TO FINAL OFFICE ACTION

MS RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

### INTRODUCTORY COMMENTS

In response to the Final Office Action dated July 23, 2007, please enter the following response, as this response is being filed along with a Request for Continued Examination (RCE):

**The Claims** are reflected in the listing of claims, which begins on page 2 of this paper.

**Remarks** begin on page 9 of this paper.